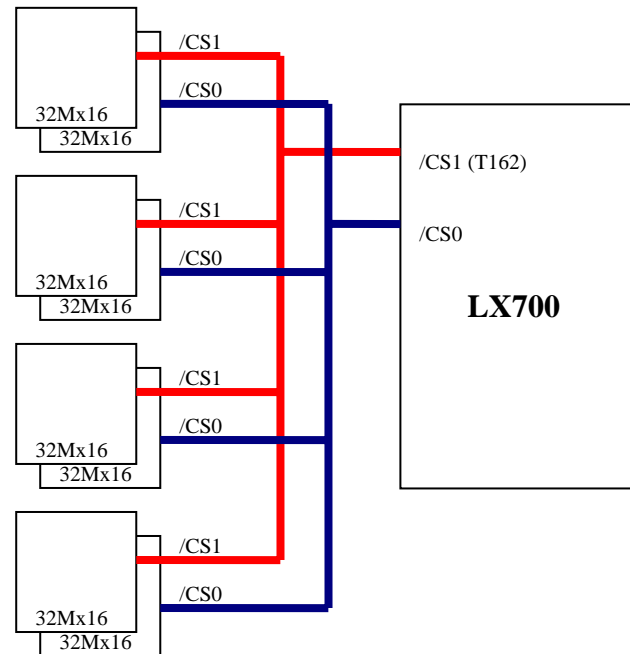


How to upgrade XO memory from 256 MB to 512 MB

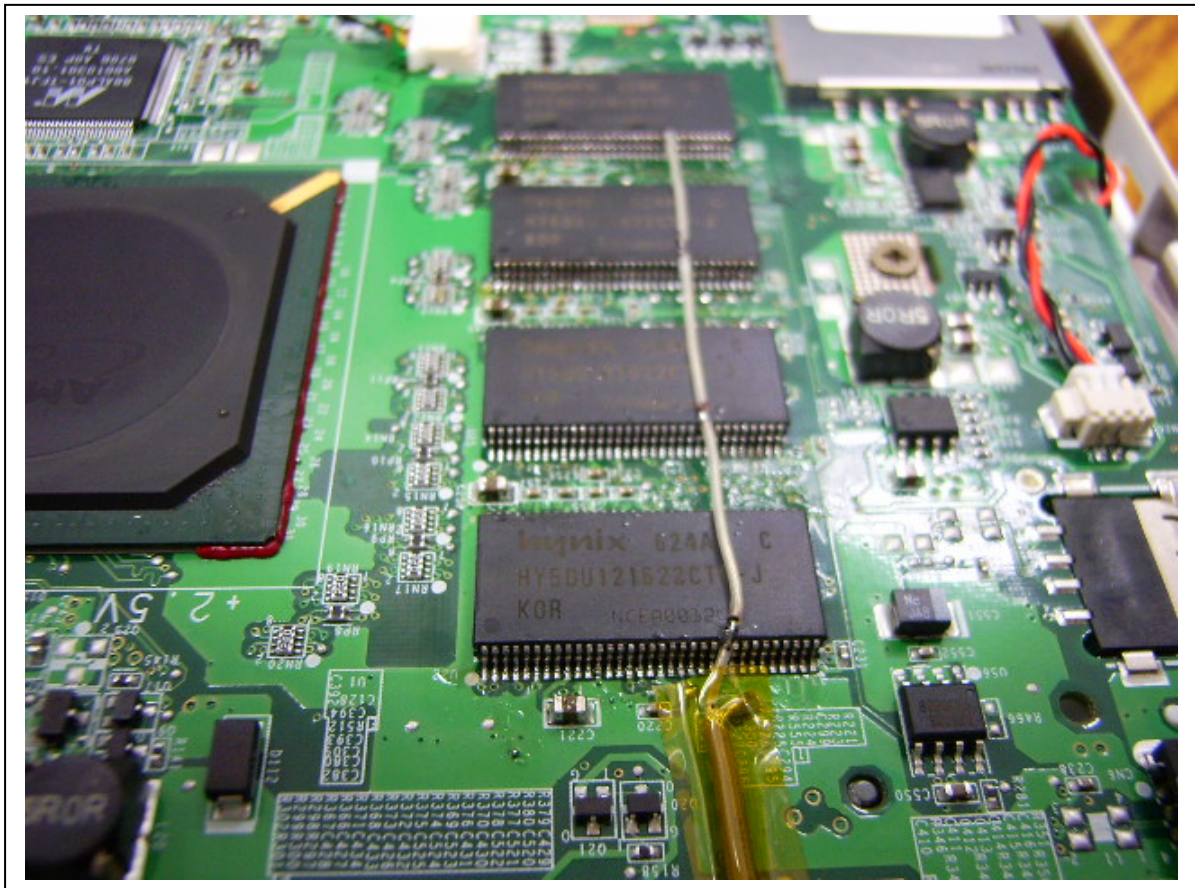
XO Current memory configuration was 32Mb x 16 x 4 = 256MByte

Soldering extra four 32MbX16 memory component on top of original memory component

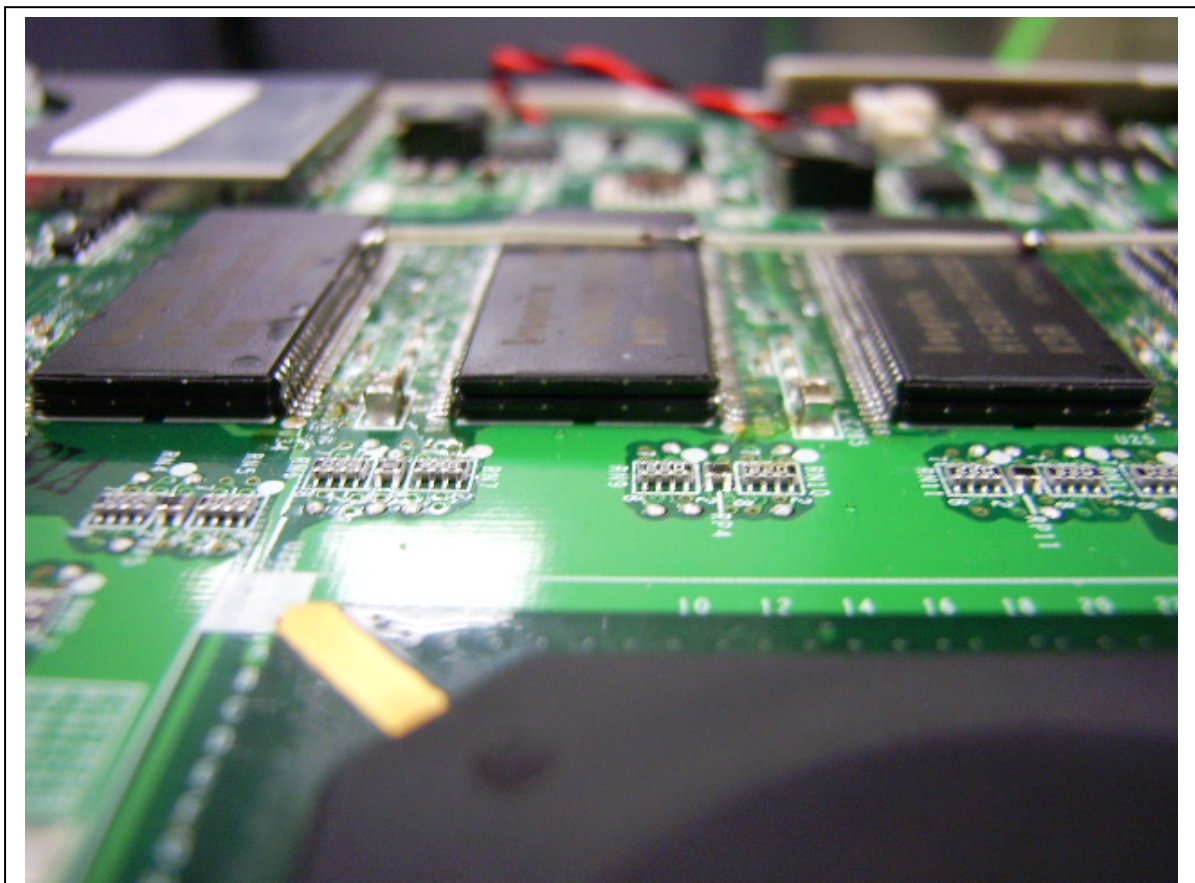
the /CS Chip select (pin 24) must connect to test point T162 (CPU memory controller /CS1)

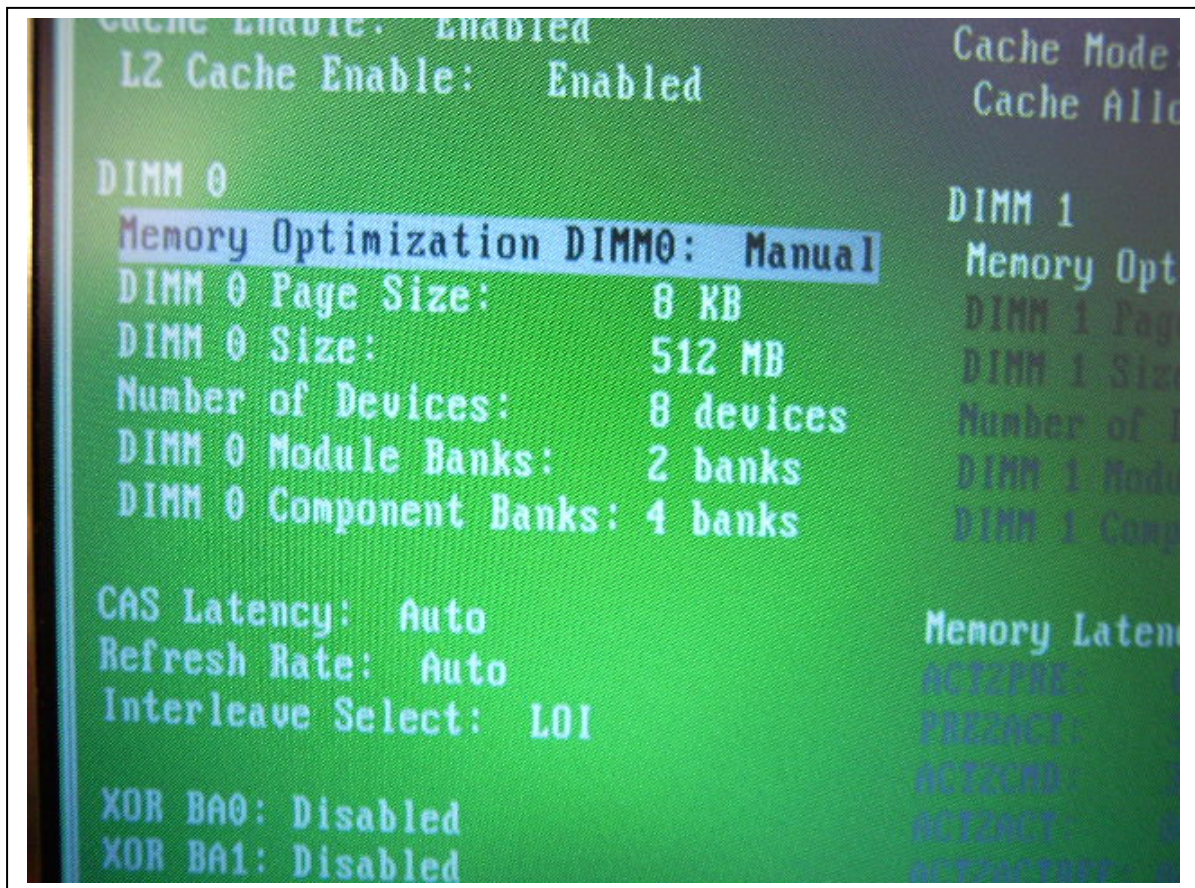


x4	x8	x16				x16	x8	x4
VDD	VDD	VDD	1		66	VSS	VSS	VSS
NC	DQ0	DQ0	2		65	DQ15	DQ7	NC
VDDQ	VDDQ	VDDQ	3		64	VSSQ	VSSQ	VSSQ
NC	NC	DQ1	4		63	DQ14	NC	NC
DQ0	DQ1	DQ2	5		62	DQ13	DQ6	DQ3
VSSQ	VSSQ	VSSQ	6		61	VDDQ	VDDQ	VDDQ
NC	NC	DQ3	7		60	DQ12	NC	NC
NC	DQ2	DQ4	8		59	DQ11	DQ5	NC
VDDQ	VDDQ	VDDQ	9		58	VSSQ	VSSQ	VSSQ
NC	NC	DQ5	10		57	DQ10	NC	NC
DQ1	DQ3	DQ6	11		56	DQ9	DQ4	DQ2
VSSQ	VSSQ	VSSQ	12		55	VDDQ	VDDQ	VDDQ
NC	NC	DQ7	13		54	DQ8	NC	NC
NC	NC	NC	14		53	NC	NC	NC
VDDQ	VDDQ	VDDQ	15	400mil X 875mil	52	VSSQ	VSSQ	VSSQ
NC	NC	LDQS	16	66pin TSOP -II	51	UDQS	DQS	DQS
NC	NC	NC	17	0.65mm pin pitch	50	NC	NC	NC
VDD	VDD	VDD	18	(Lead free)	49	VREF	VREF	VREF
NC	NC	NC	19		48	VSS	VSS	VSS
NC	NC	LDM	20		47	UDM	DM	DM
/WE	/WE	/WE	21		46	/CK	/CK	/CK
/CAS	/CAS	/CAS	22		45	CK	CK	CK
/RAS	/RAS	/RAS	23		44	CKE	CKE	CKE
/CS	/CS	/CS	24		43	NC	NC	NC
NC	NC	NC	25		42	A12	A12	A12
BA0	BA0	BA0	26		41	A11	A11	A11
BA1	BA1	BA1	27		40	A9	A9	A9
A10/AP	A10/AP	A10/AP	28		39	A8	A8	A8
A0	A0	A0	29		38	A7	A7	A7
A1	A1	A1	30		37	A6	A6	A6
A2	A2	A2	31		36	A5	A5	A5
A3	A3	A3	32		35	A4	A4	A4
VDD	VDD	VDD	33		34	VSS	VSS	VSS

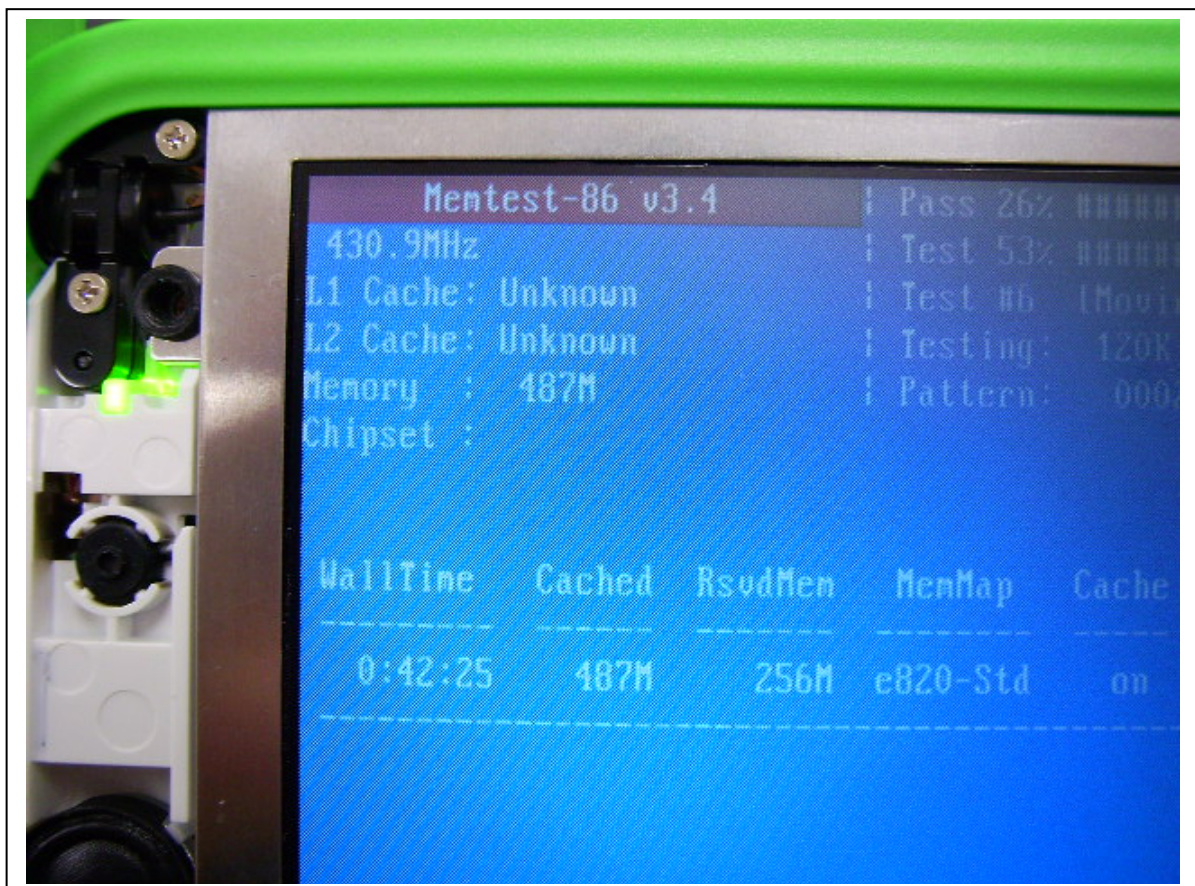


**Use Coaxial cable and shelling GND will increase signal quality.
(Memory Chip select signal speed almost up to 100Mhz)**





BIOS setting memory configuration by Manual (Insyde Embedded BIOS)



Running Memtest-86 V3.4 almost 200 hours without any Error.

This is very popular design on DDR1 DIMM module field for cost down purpose

http://www.valueram.com/datasheets/KVR266X72RC25L_2G.pdf